

TO SEPARATE, HOLD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>09-998,714</b>	GROUP/ART UNIT <b>2817</b>	ATTACHMENT TO PAPER NUMBER	<b>6</b>
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Brosh et al.</b>			
U.S. PATENT DOCUMENTS							
•	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
A	<b>6150853</b>	<b>11-21-2000</b>	<b>Chrappan et al.</b>	<b>327</b>	<b>108</b>		
B	<b>5378938</b>	<b>1-3-1995</b>	<b>Birdsall et al.</b>	<b>327</b>	<b>94</b>		
C							
D							
E							
F							
G							
H							
I							
J							
K							
FOREIGN PATENT DOCUMENTS							
•	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG. PP. SPEC.
L	<del>5378938</del>	<b>3-5-1988</b>	<b>Japan</b>	<b>Soneda</b>	<b>—</b>	<b>—</b>	
M	<b>2294306</b>						
N							
O							
P							
Q							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
R	<b>Millman "Microelectronics" McGraw-Hill Book Co</b>						
	<b>1979 pp 670-671</b>						
S	<b>E/wan et al. "CMOS low-voltage class-AB operational</b>						
	<b>transconductance amplifier" Electronics Letters, vol. 36 Issue 17</b>						
T	<b>Aug 17, 2000 pp 1439-1440</b>						
U							
EXAMINER			DATE				
<b>SHINGLETON</b>			<b>10-31-02</b>				
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)							